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17236 U.S.PTO

Atty. Dkt. No. 039153-0667
(AMD H0981)

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17497 U.S.PTO
10/684727

101403
Barcode

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Qi XIANG

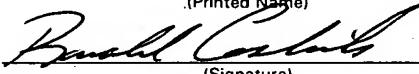
Title: STRAINED SILICON MOSFET
HAVING IMPROVED
CARRIER MOBILITY,
STRAINED SILICON CMOS
DEVICE, AND METHODS OF
THEIR FORMATION

Appl. No.: Unknown

Filing Date: Unknown

Examiner: Unknown

Art Unit: Unknown

CERTIFICATE OF EXPRESS MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450.	
EV 060046680 US (Express Mail Label Number)	October 14, 2003 (Date of Deposit)
Ronald Coslick (Printed Name)	
	
(Signature)	

UTILITY PATENT APPLICATION
TRANSMITTAL

Mail Stop PATENT APPLICATION
Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Sir:

Transmitted herewith for filing under 37 C.F.R. § 1.53(b) is the nonprovisional utility patent application of:

Qi XIANG
1119 Thames Dr.
San Jose, California 95129

[] Applicant claims small entity status under 37 CFR 1.27.

Enclosed are:

[X] Specification, Claim(s), and Abstract (19 pages).

[X] Formal drawings (6 sheets, Figures 1, 2, 3a, 3b, 3c, 3d, 3e, 3f, 3g, 3h, 3i, 3j, 4, 5, 6).

[X] Declaration and Power of Attorney (3 pages).

[X] Assignment of the invention to Advanced Micro Devices, Inc..

[X] Assignment Recordation Cover Sheet.

[X] Check in the amount of \$40.00 for Assignment recordation.

[] Small Entity statement.

[X] Request for application not to be published with certification under 35 USC 122(b)(2)(B)(i).

[] Information Disclosure Statement.

[] Form PTO/SB/08 with copies of ___ listed reference(s).

[X] Application Data Sheet (37 CFR 1.76).

[] Claim for Convention Priority.

The filing fee is calculated below:

	Claims as Filed	Included in Basic Fee	Extra Claims	Rate	Fee Totals
Basic Fee				\$770.00	\$770.00
Total Claims:	26	- 20 = 6	x \$18.00	= \$108.00	
Independents:	4	- 3 = 1	x \$86.00	= \$86.00	
If any Multiple Dependent Claim(s) present:			+ \$290.00	= \$0.00	
			SUBTOTAL:	= \$964.00	
[] Small Entity Fees Apply (subtract ½ of above):				= \$0.00	
			TOTAL FILING FEE:	= \$964.00	

[X] A check in the amount of \$964.00 to cover the filing fee is enclosed.

[] The required filing fees are not enclosed but will be submitted in response to the Notice to File Missing Parts of Application.

[X] The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 50-0872. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated,

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otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 50-0872.

Please direct all correspondence to the undersigned attorney or agent at the address indicated below.

Respectfully submitted,

By Ronald Coslick

Date October 14, 2003

FOLEY & LARDNER
Customer Number: 23392
Telephone: (310) 975-7964
Facsimile: (310) 557-8475

Ronald Coslick
Attorney for Applicant
Registration No. 36,489

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Applicant: Qi XIANG

Title: STRAINED SILICON MOSFET
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REQUEST AND CERTIFICATION UNDER 35 U.S.C. 122(b)(2)(B)(i)

Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicants hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

Applicants hereby request that the attached application not be published under 35 U.S.C. 122(b).

Respectfully submitted,

Date October 14, 2003

By Ronald Coslick

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Ronald Coslick
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